

REMARKS

Claims 6-13 are all the claims pending in the application. Applicants amend claim 1.

Claim rejections

Claims 6, 8-11 are rejected under 35 U.S.C. § 102(e) as allegedly being anticipated by Park (U.S. PG Pub. No. 2004/0004443).

Claims 12 and 13 are rejected under 35 U.S.C. § 103(a) as allegedly being unpatentable over Park in view of Kuwabara (US PG Pub No. 2003/0141504) and further in view of Veres (U.S. PG Pub. No. 2006/0105492).

Applicants traverse the rejection for at least the following reasons.

Claim 1

Claim 1 recites, *inter alia*, “wherein the plurality of thin film transistors are arranged such that their channels are positioned in parallel to each another.” The Examiner indicates that in paragraph [0031], lines 13-15 Park disclose these features of claim 1. Applicants respectfully disagree with the Examiner’s assertion for at least the following reasons.

Park is directed to an organic electro-luminescence device and a method of driving the device wherein deterioration of the driving thin film transistor is prevented (paragraph [0003]). Park discloses an organic electro-luminescence device that includes column lines to which data voltage are applied, row lines to which scan voltages are applied and cells formed at pixel areas defined between column lines and row lines. The cell includes a first switching device and a second switching device for controlling a current applied to the cell in response to the data voltages, wherein the second switching device is connected to the cell in parallel with the first

switching device. However, Park does not disclose a plurality of thin film transistors arranged such that their channels are positioned in parallel to each another.

In particular, paragraph [0031], lines 13-15 states that “wherein the second switching device is connected to the cell in parallel with the first switching device”. This portion does not disclose that the second switching device is positioned in parallel with the first switching device. On the contrary, this cited portion corresponding to the circuit diagram shown in FIG. 9, discloses that the second switching device and the cell are electrically connected in parallel with the first switching device. That is, in the circuit diagram shown in FIG. 9, the second switching device (T2) is electrically connected to the cell (OLED), and the second switching device (T2) and the cell (OLED) are electrically connected with the first switching device (T1).

As such, Park merely discloses that the second switching device and the first switching device are electrically connected in parallel with each other and does not disclose or remotely suggest the plurality of thin film transistors are arranged such that their channels are positioned in parallel to each another

In view of the above, Applicants submit that claim 1 is allowable over the cited reference.

Claims 7-11

Applicants submit that claims 7-11 depend from claim 6, and therefore are allowable at least by virtue of their dependency.

Claims 12 and 13

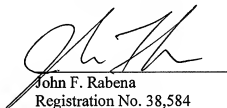
Applicants submit that since Kawabura and Veres does not disclose the features missing in Park, and since claims 12 and 13 depend from claim 1, these claims are allowable at least by virtue of their dependency.

Conclusion

In view of the above, reconsideration and allowance of this application are now believed to be in order, and such actions are hereby solicited. If any points remain in issue which the Examiner feels may be best resolved through a personal or telephone interview, the Examiner is kindly requested to contact the undersigned at the telephone number listed below.

The USPTO is directed and authorized to charge all required fees, except for the Issue Fee and the Publication Fee, to Deposit Account No. 19-4880. Please also credit any overpayments to said Deposit Account.

Respectfully submitted,



John F. Rabena
Registration No. 38,584

SUGHRUE MION, PLLC
Telephone: (202) 293-7060
Facsimile: (202) 293-7860

WASHINGTON OFFICE

23373

CUSTOMER NUMBER

Date: July 30, 2008